

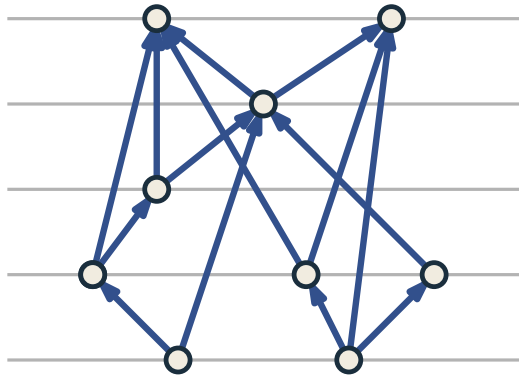
Planar L-Drawings of Directed Graphs

Steven Chaplick, Markus Chimani, Sabine Cornelsen,
Giordano Da Lozzo, Martin Nöllenburg, Maurizio Patrignani,
Ioannis G. Tollis, Alexander Wolff

Graph Drawing and Network Visualization 2017 · Boston

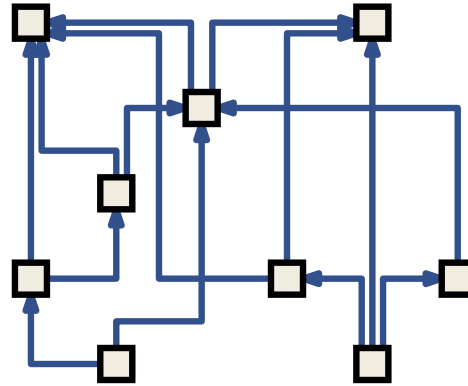
Drawing Directed Graphs

There is a variety of drawing styles for directed graphs, e.g.



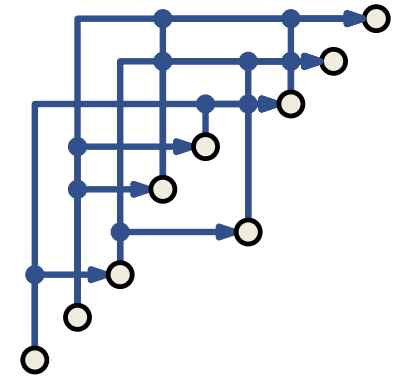
Layered layout

[Sugiyama, Tagawa, Toda 1981]



Kandinsky style layout

[Föbmeier, Kaufmann 1996]

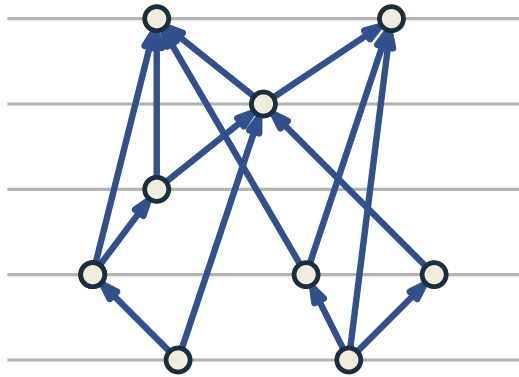


Overloaded
orthogonal layout

[Kornaropoulos, Tollis 2011]

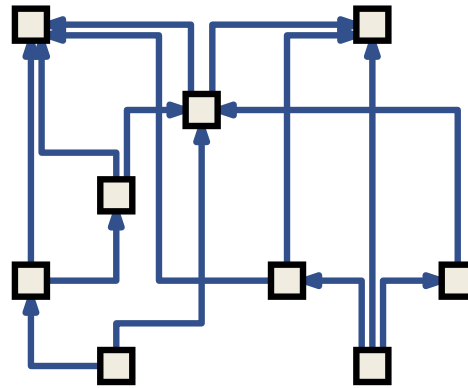
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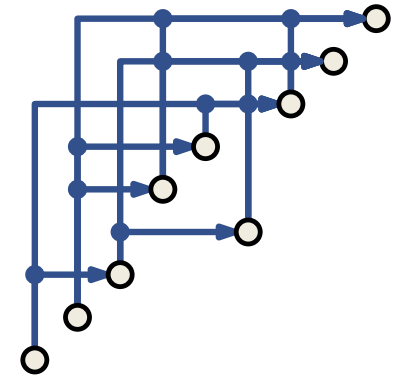
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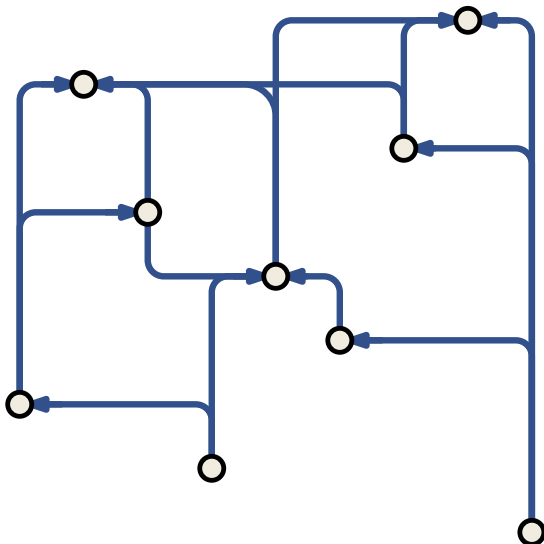
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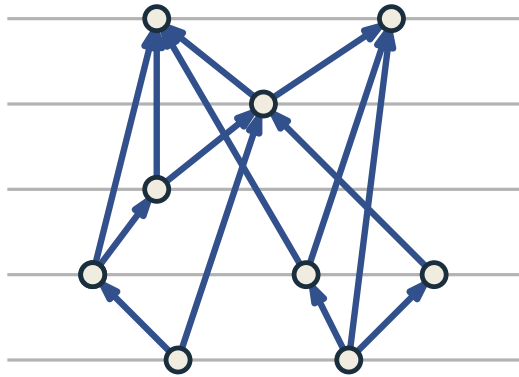
In 2016 Angelini et al. introduced **L-drawings**:



- exclusive x- and y-coordinates per vertex
- outgoing edges attach vertically
- incoming edges attach horizontally
- small arcs indicate L-bends
- crossings and “confluent” overlaps allowed
- exist for any graph
- ink minimization is NP-hard

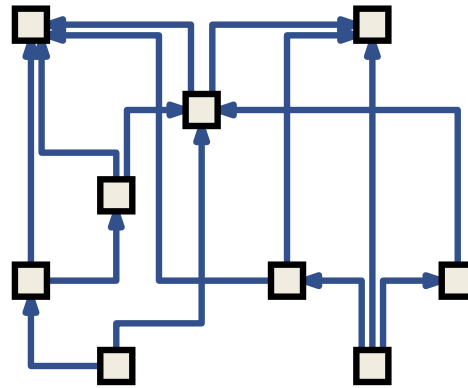
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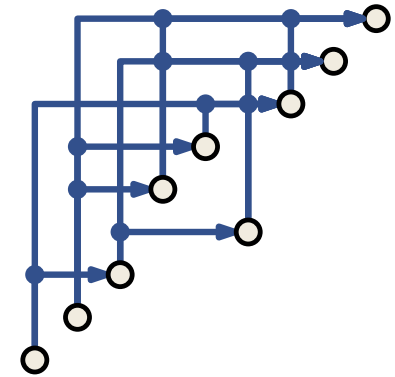
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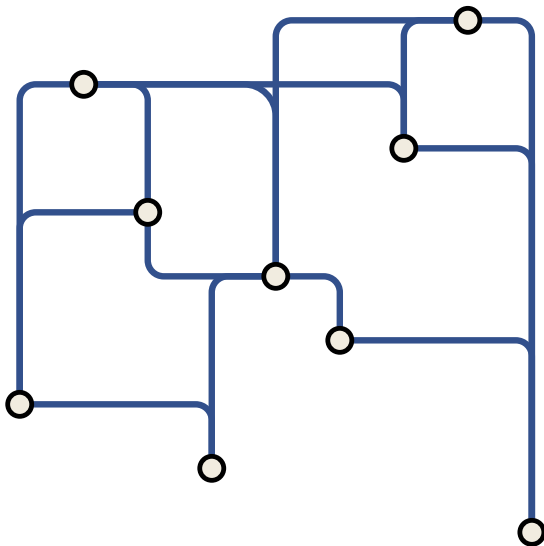
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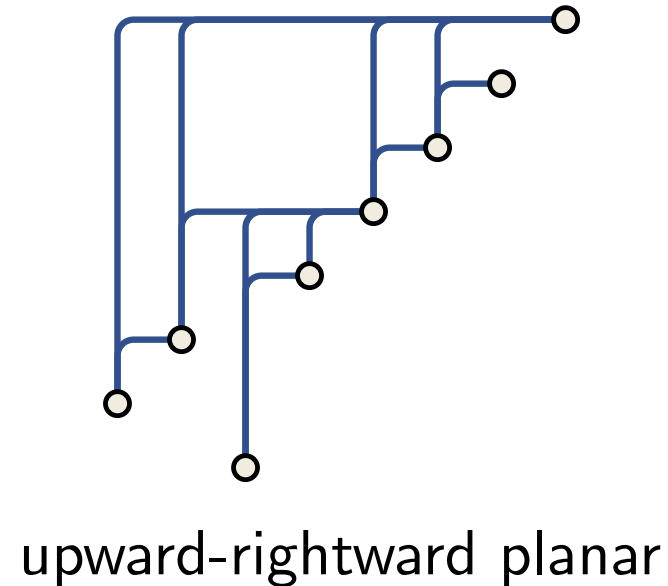
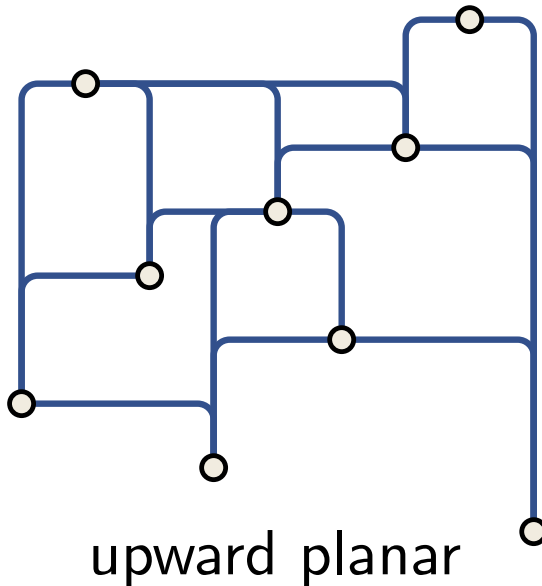
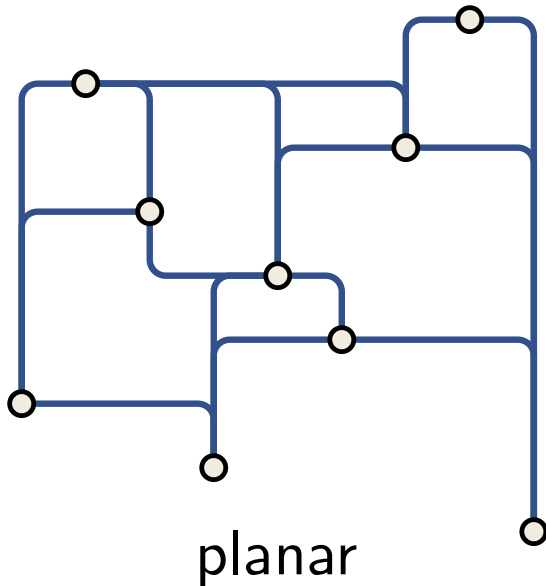
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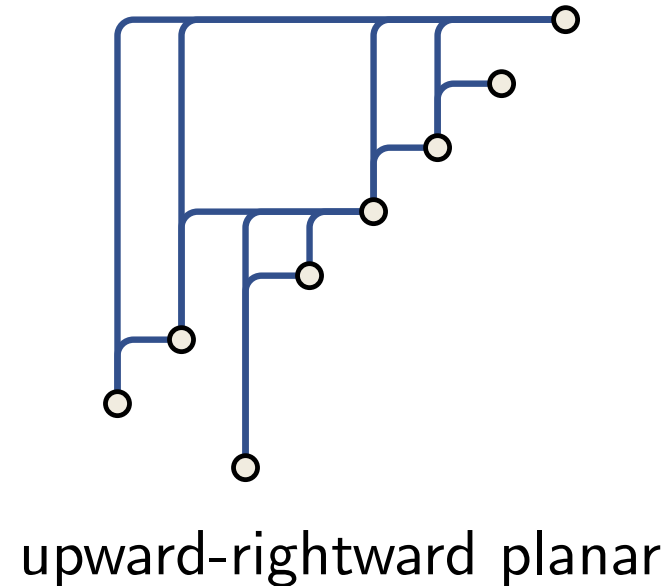
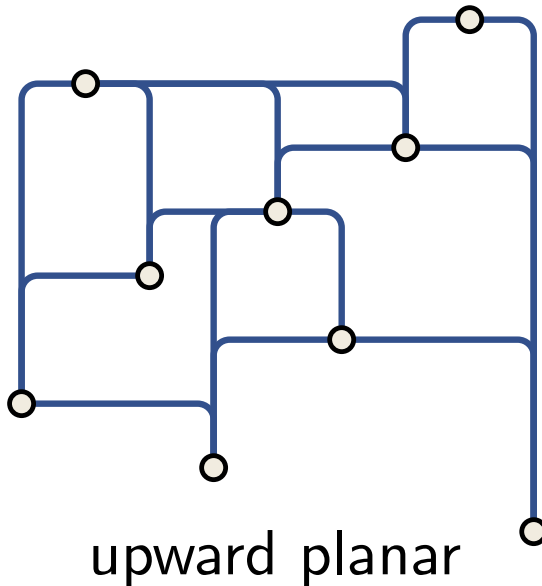
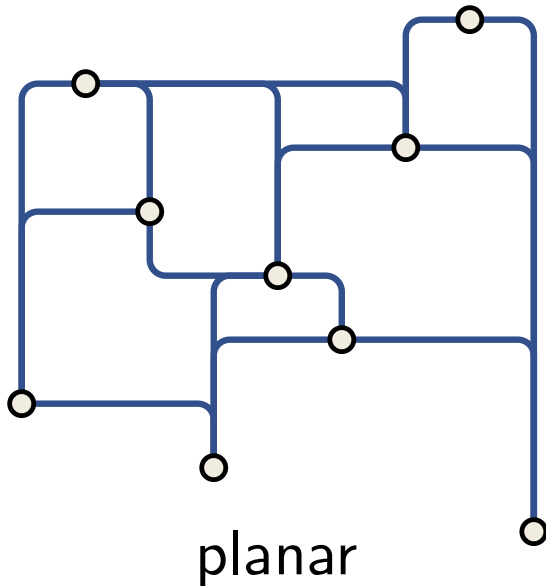
Planar L-Drawings



Definitions:

- **Planar L-drawing** if crossing-free
- **Upward planar L-drawing** if all edges y-increasing
- **Upward-rightward planar L-drawing** if all edges x- and y-increasing

Planar L-Drawings

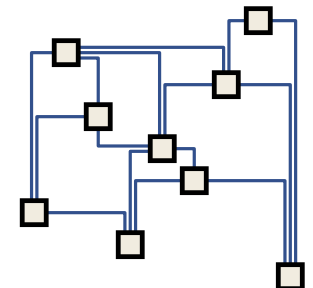
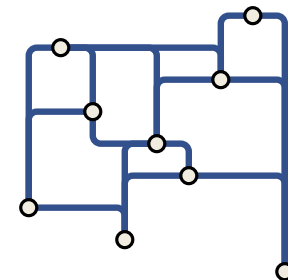


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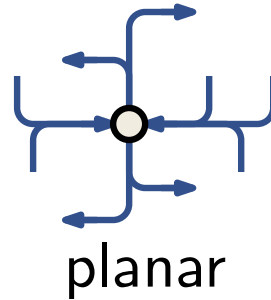
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Observation:

Planar L-drawings correspond to planar 1-bend Kandinsky drawings with extra constraints on cyclic edge orders of vertices.



Overview of Results



directed
planar graphs

NP-complete

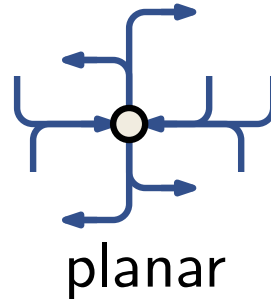
planar *st*-graphs

characterization
constructive linear time algorithm

directed plane graphs
+ port assignment

linear time

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upward (-rightward) planar

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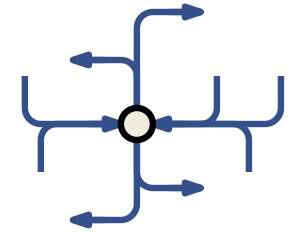
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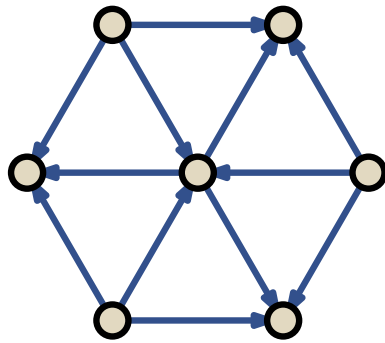
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Planar L-Drawings of Directed Graphs

Any planar L-drawing implies 4-modal embedding.



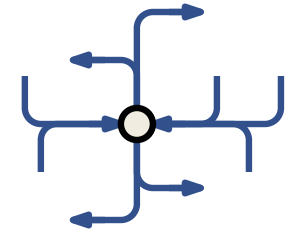
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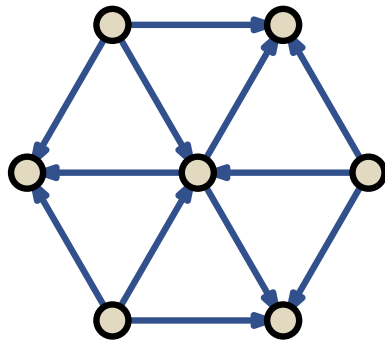
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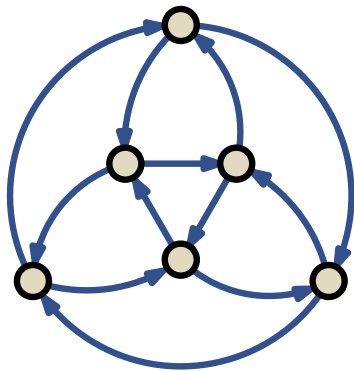


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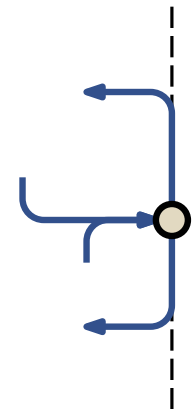
- There are graphs with 4-modal embedding but no planar L-drawing.



octahedron

every vertex is 4-modal ...

... but rightmost vertex in L-drawing
can be at most bimodal



Theorem: Deciding whether a directed graph admits a planar L-drawing is NP-complete.

Proof: (sketch)

- reduction from NP-complete **HV-rectilinear planarity testing**

[Didimo, Liotta, Patrignani 2014]

Given biconnected degree-4 planar graph G with edges labeled H and V , decide if G admits drawing with horizontal H -edges and vertical V -edges.

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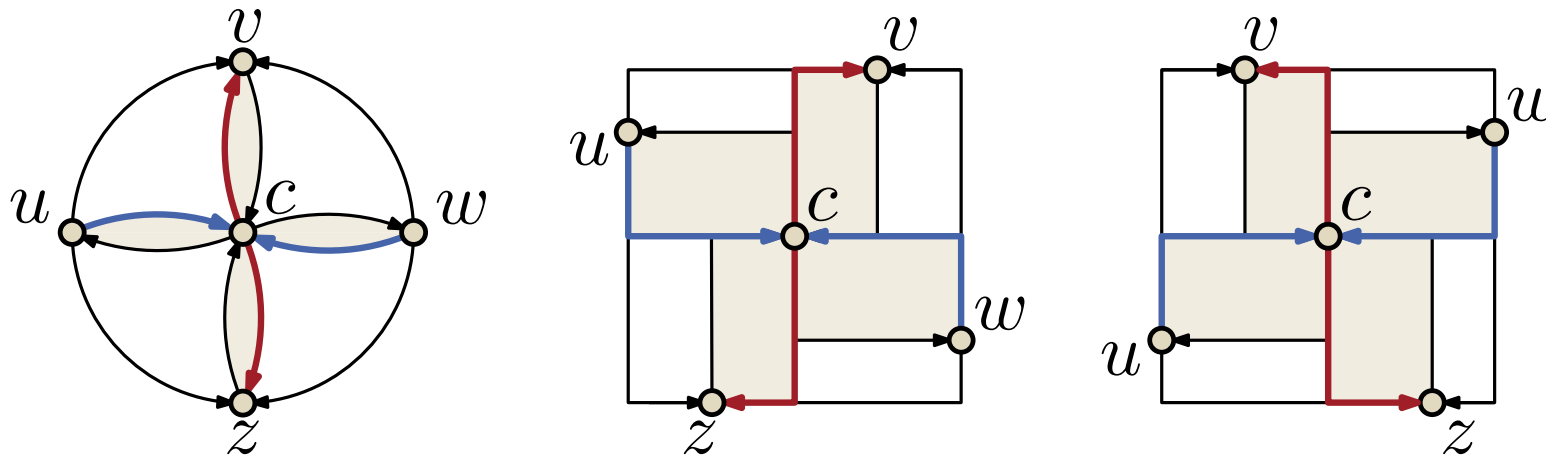
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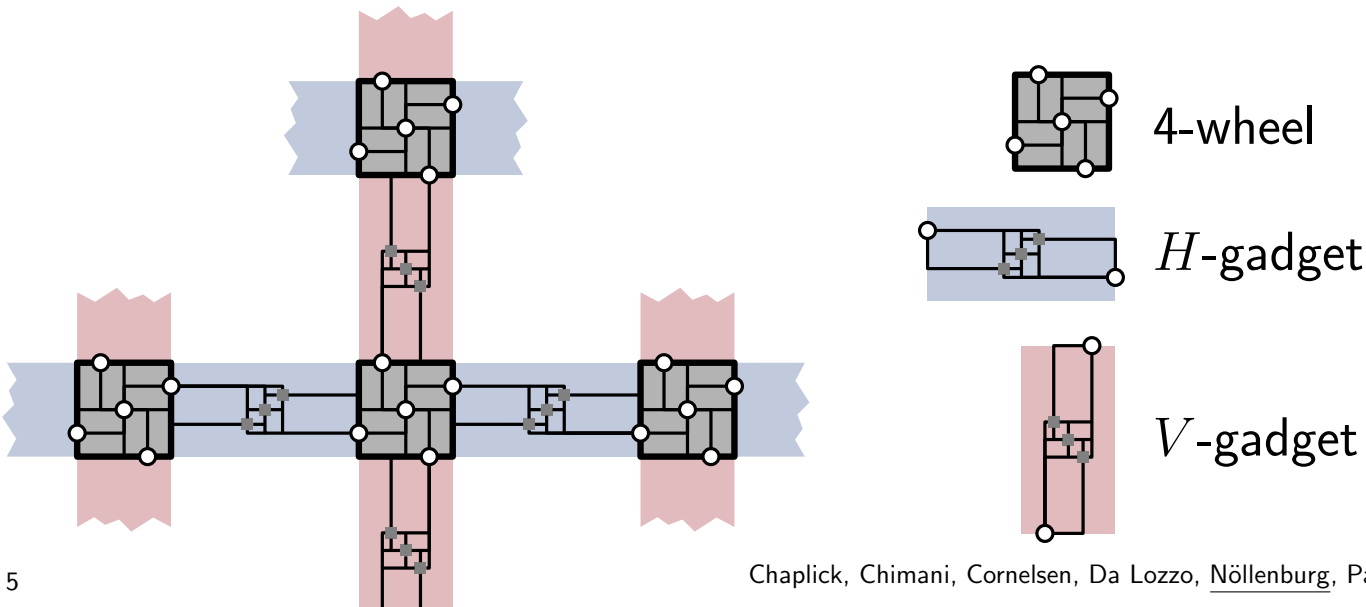
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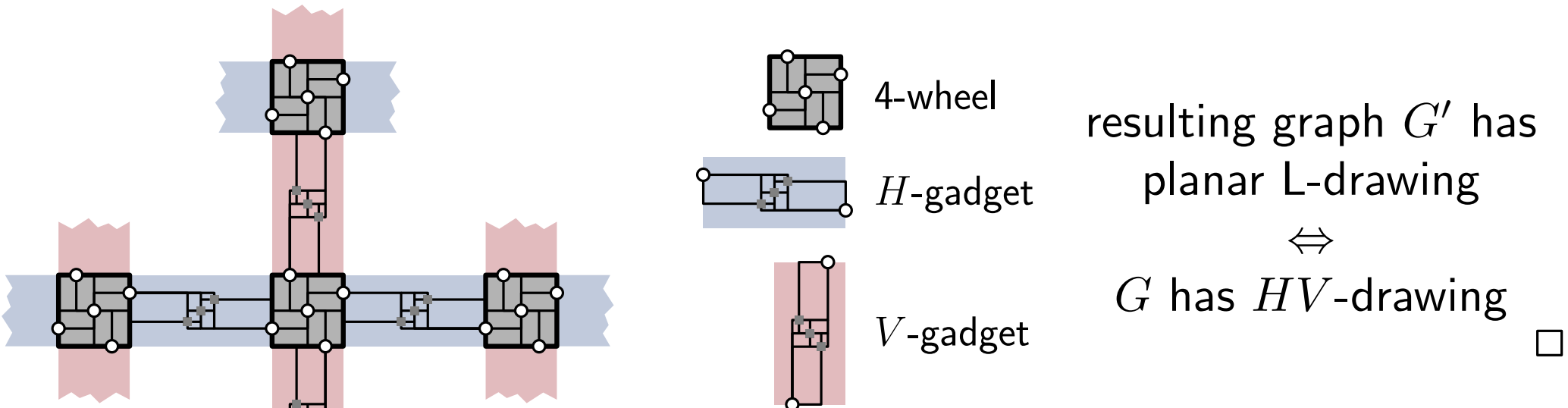
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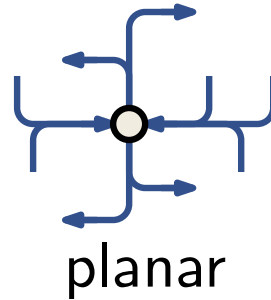
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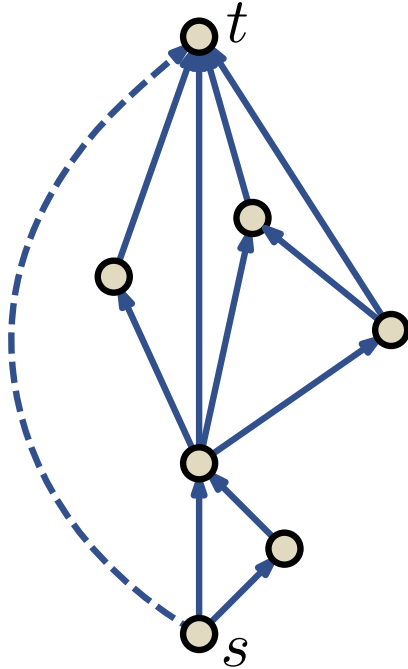
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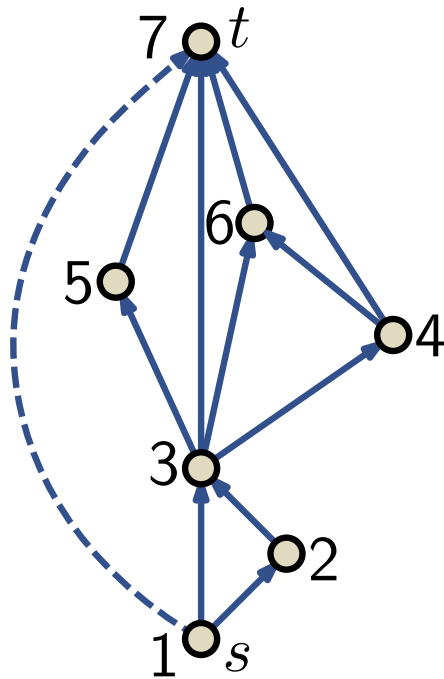
Planar st -Graphs and Bitonic st -Orderings

- A **planar st -graph** G is a directed acyclic graph with exactly one source s and one sink t , both embeddable on same face.



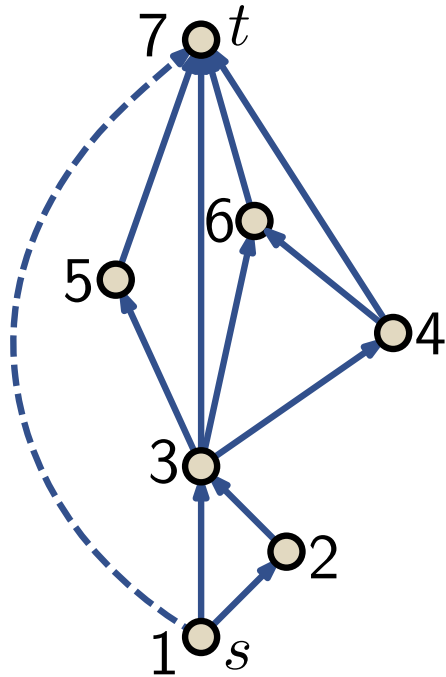
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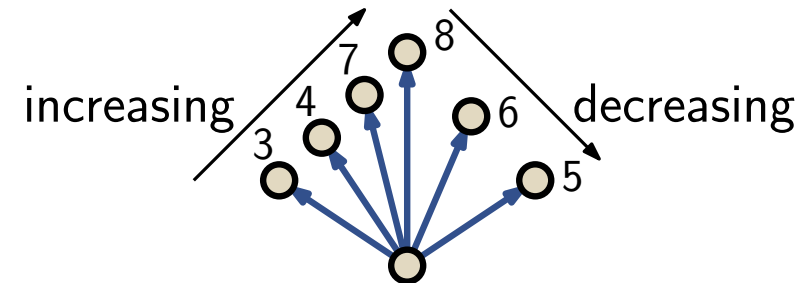


- planar st -graphs always admit straight-line upward planar drawings [Di Battista, Tamassia 1988]
- have **st -ordering** π respecting edge directions

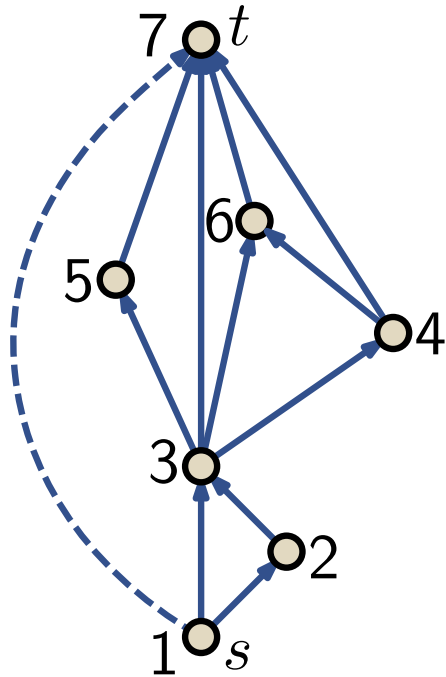
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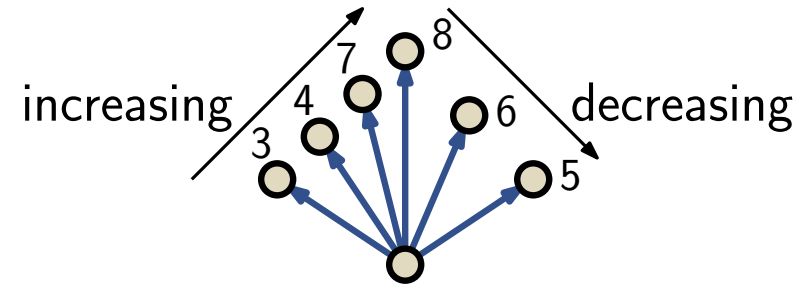
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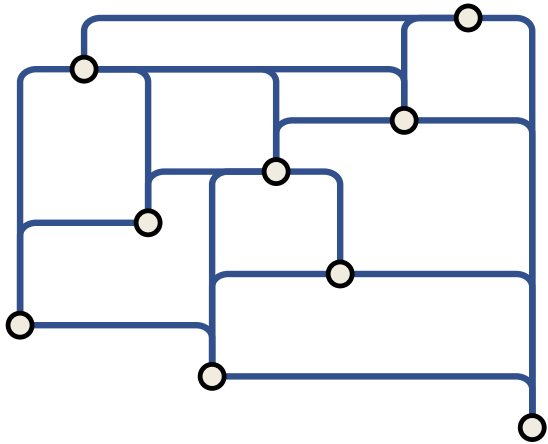
- For a planar st -graph G define a **bitonic pair** (\mathcal{E}, π) as an upward planar embedding \mathcal{E} of G with a bitonic st -ordering π .

Theorem: A planar st -graph admits an upward-planar L-drawing if and only if it admits a bitonic pair.

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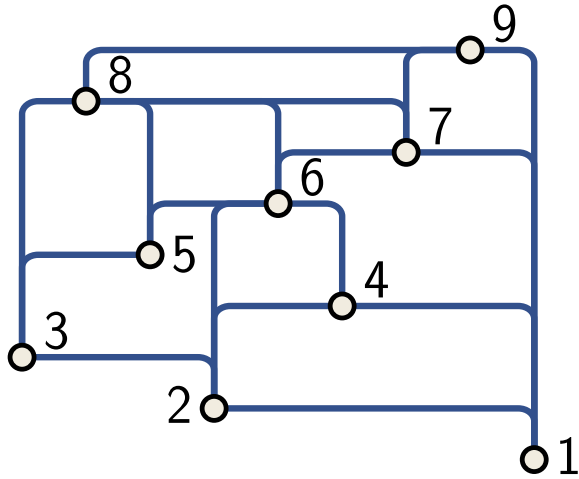
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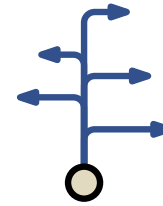
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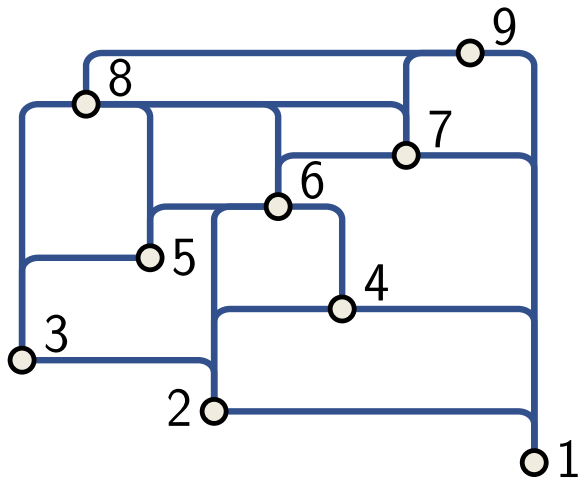
- y-coordinates induce st -ordering π
- π is bitonic due to upward L-properties



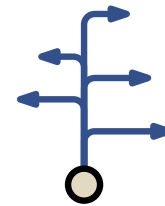
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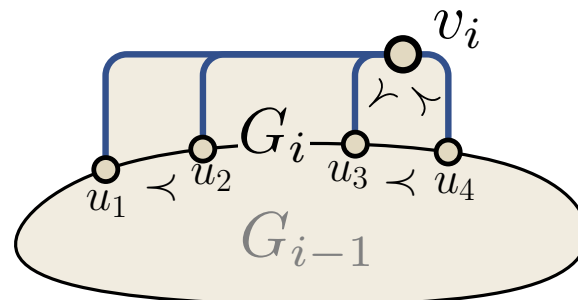
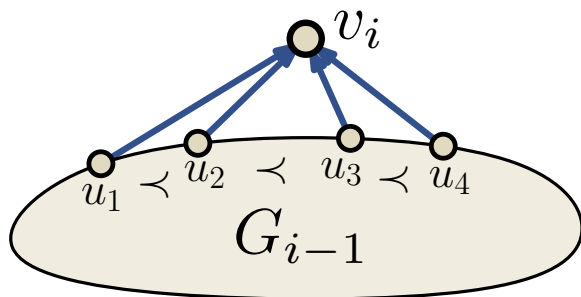


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\Leftarrow

- use π for y-coordinates
- incrementally construct partial order \prec as basis for x-coordinates

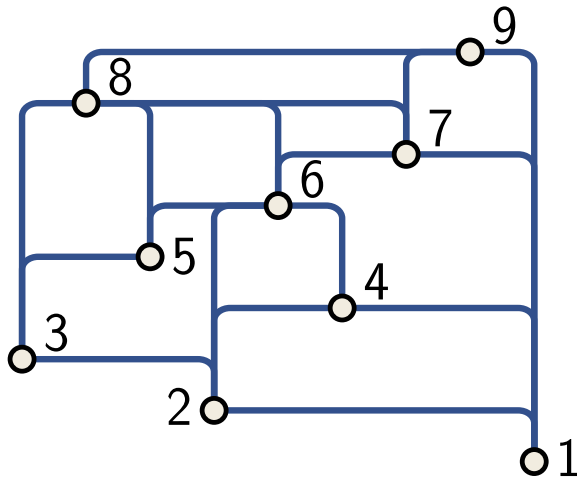


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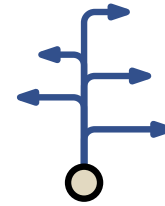
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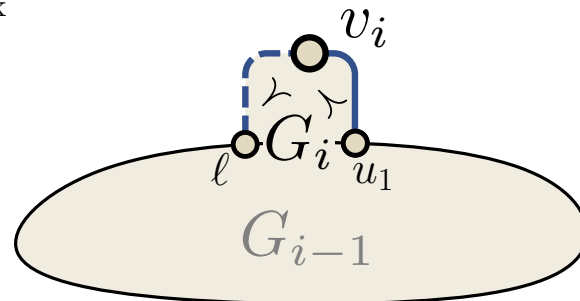
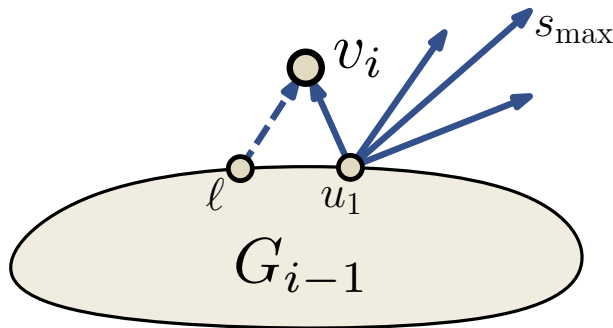


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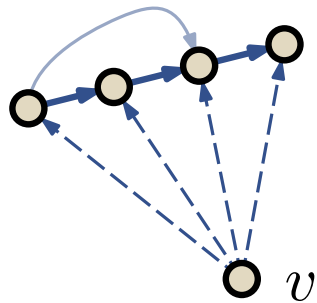


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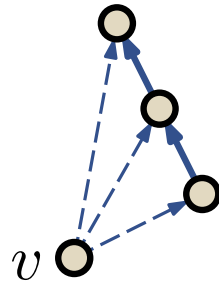
special case: just one predecessor \rightarrow augment graph similar to [Gronemann 2016]

Finding Bitonic Pairs

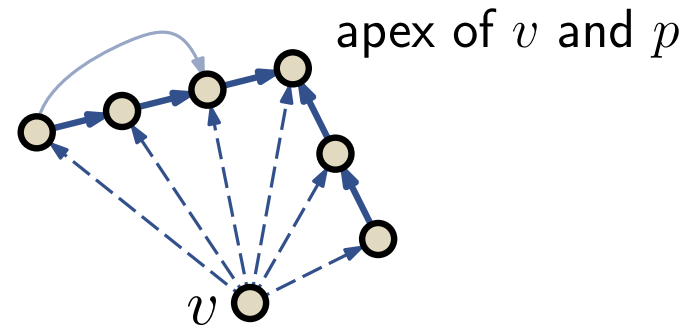
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monotonic \subset **bitonic**

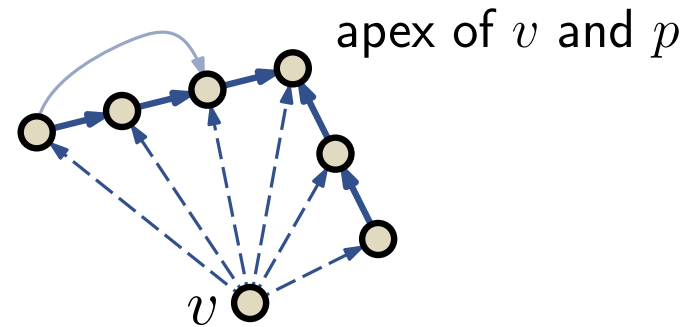
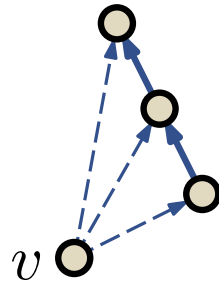
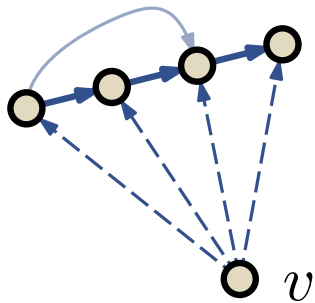


strictly bitonic \subset **bitonic**



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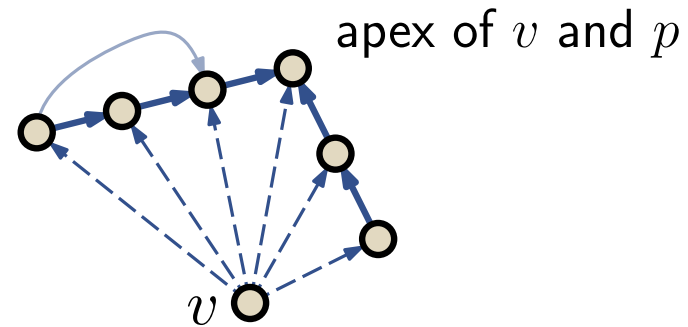
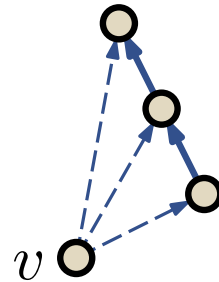
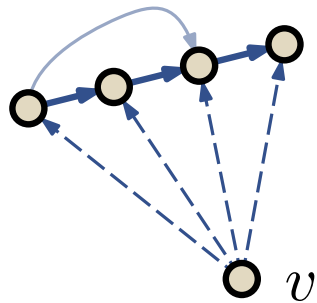
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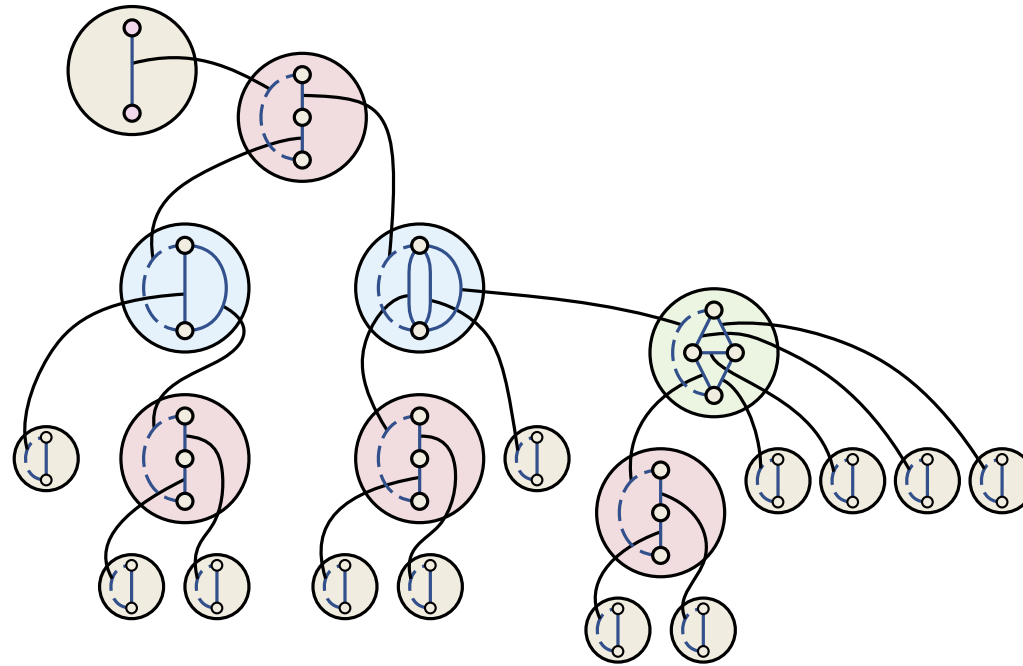
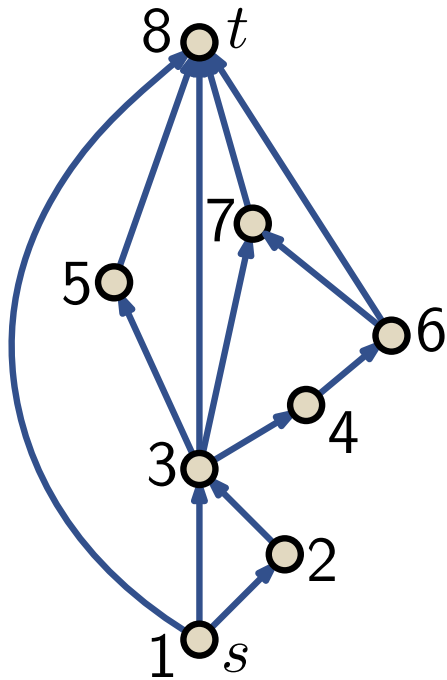
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Theorem: Plane st -graph G admits bitonic st -ordering iff G^* is v -bitonic.
Any st -ordering of G^* is bitonic st -ordering of G . ~ [Gronemann 2016]

→ The task of finding a bitonic pair of G reduces to finding an augmentation G^* of G that is v -bitonic.

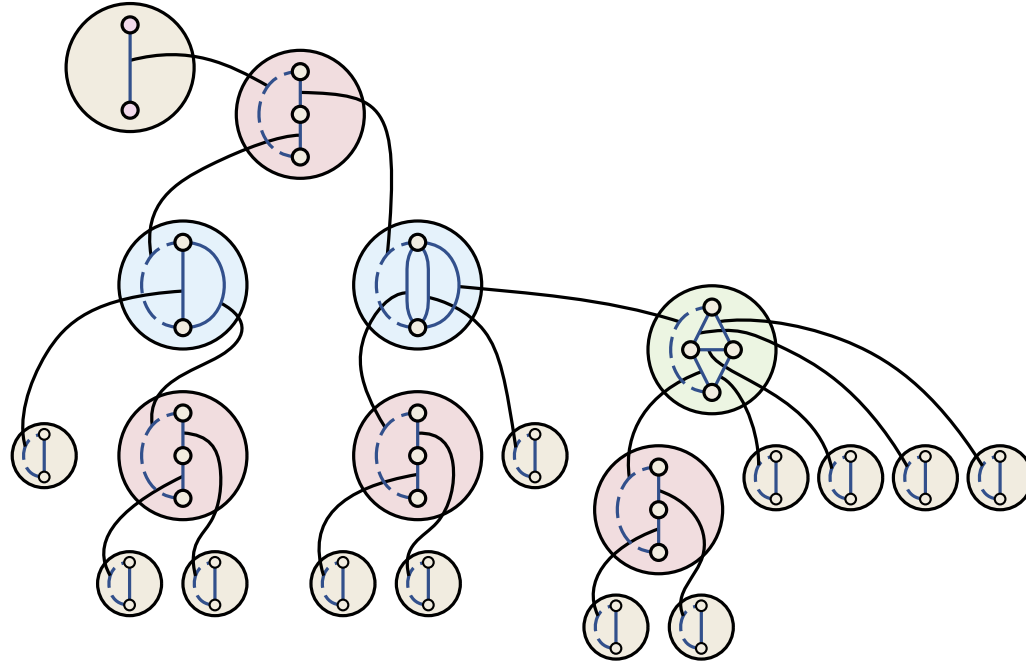
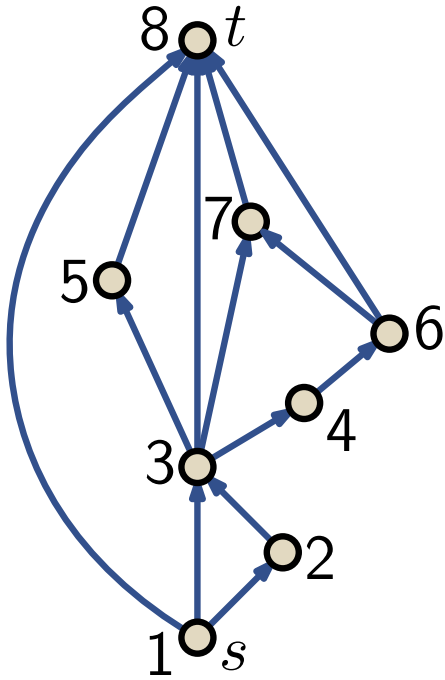
Finding a v -Bitonic Augmentation

Visiting the SPQR-tree of biconnected planar st -graph G rooted at edge (s, t) in bottom-up fashion find augmentation G^* and embedding (if one exists).



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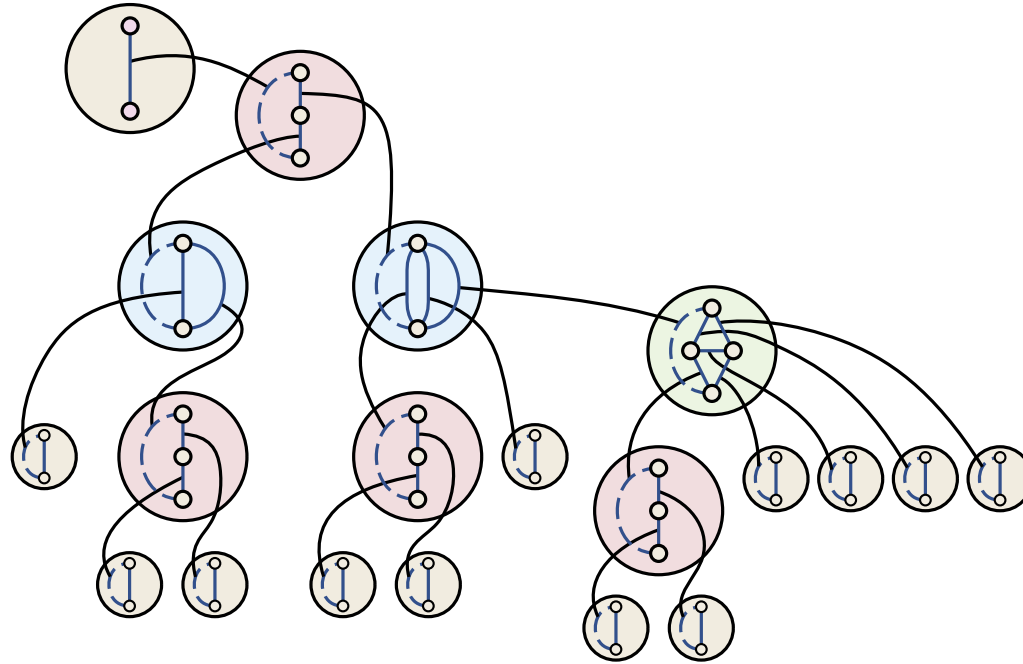
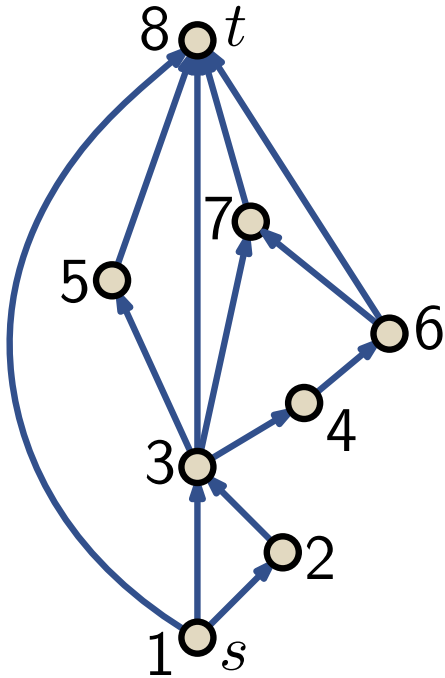


An SPQR-node μ with source s_μ is of

- **type M** if the augmented pertinent graph is s_μ -monotonic
- **type B** if the augmented pertinent graph is strictly s_μ -bitonic

Finding a v -Bitonic Augmentation

Visiting the SPQR-tree of biconnected planar st -graph G rooted at edge (s, t) in bottom-up fashion find augmentation G^* and embedding (if one exists).

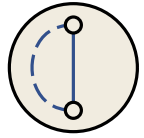


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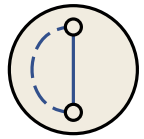
When processing an SPQR-node μ our primary goal is to make it type M and otherwise type B. If both fails, no v -bitonic augmentation of G exists.

Processing SPQR-Nodes

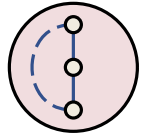


Q-node: trivially type M

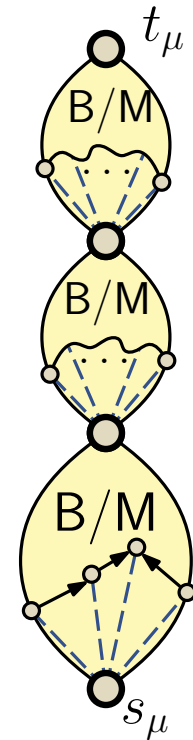
Processing SPQR-Nodes



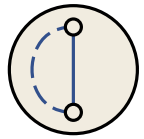
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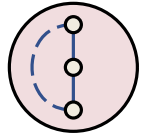
S-node: replace each virtual edge by augmented pertinent graph of child node with arbitrarily flipped embedding.
Node type is inherited from bottom child.



Processing SPQR-Nodes

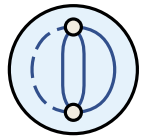


Q-node: trivially type M



S-node: replace each virtual edge by augmented pertinent graph of child node with arbitrarily flipped embedding.

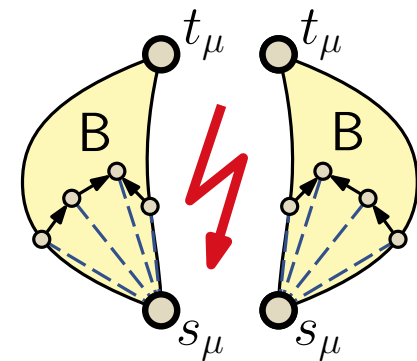
Node type is inherited from bottom child.

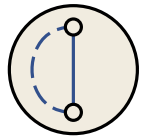


P-node:

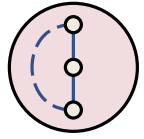
- if two or more children are of type B

→ successors of s_μ have two apices, so regardless of embedding no s_μ -bitonic augmentation exists



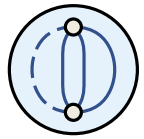


Q-node: trivially type M



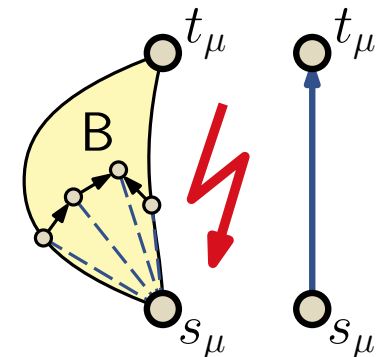
S-node: replace each virtual edge by augmented pertinent graph of child node with arbitrarily flipped embedding.

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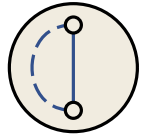


P-node:

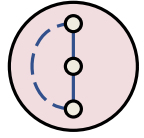
- if two or more children are of type B
→ successors of s_μ have two apices, so regardless of embedding no s_μ -bitonic augmentation exists
- if one child is of type B and one child is Q-node for (s_μ, t_μ)
→ apex of type-B node $\neq t_\mu$, but t_μ must be apex of s_μ ;
again s_μ has two apices and no s_μ -bitonic augmentation exists



Processing SPQR-Nodes

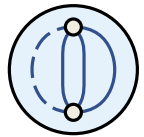


Q-node: trivially type M

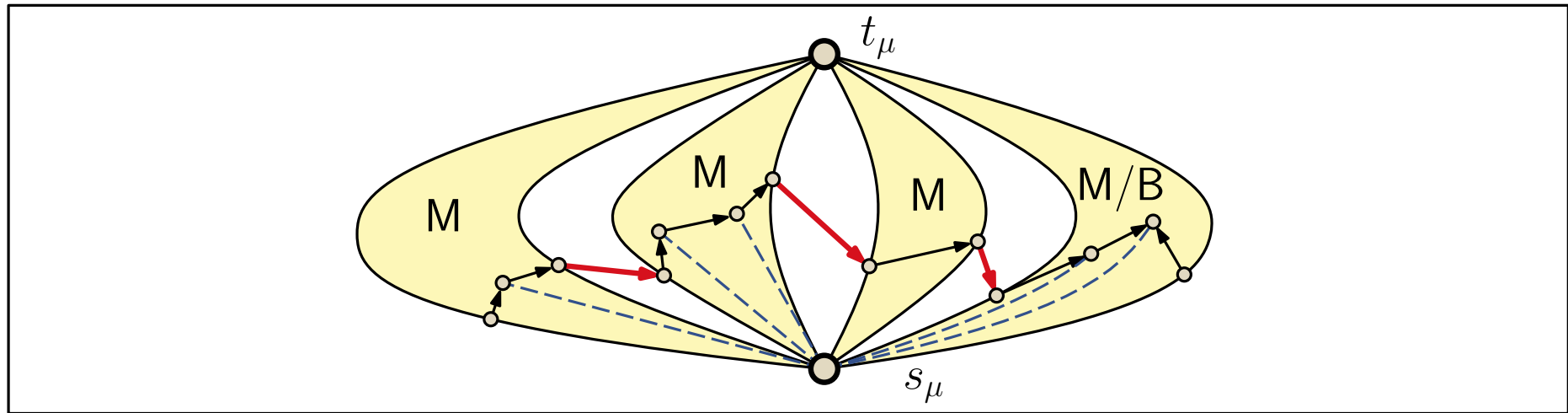


S-node: replace each virtual edge by augmented pertinent graph of child node with arbitrarily flipped embedding.

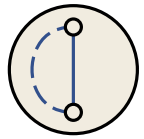
Node type is inherited from bottom child.



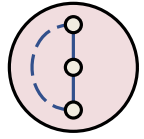
P-node:



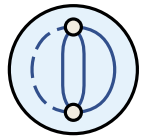
- else embed child of type B or Q-node for (s_μ, t_μ) rightmost (if any) and connect successors of s_μ in order of embedding; node type is M \Leftrightarrow rightmost child is of type M



Q-node: trivially type M

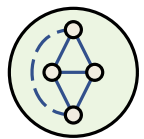


S-node: replace each virtual edge by augmented pertinent graph of child node with arbitrarily flipped embedding.
Node type is inherited from bottom child.



P-node:

- if two or more children are of type B
→ successors of s_μ have two apices, so regardless of embedding no s_μ -bitonic augmentation exists
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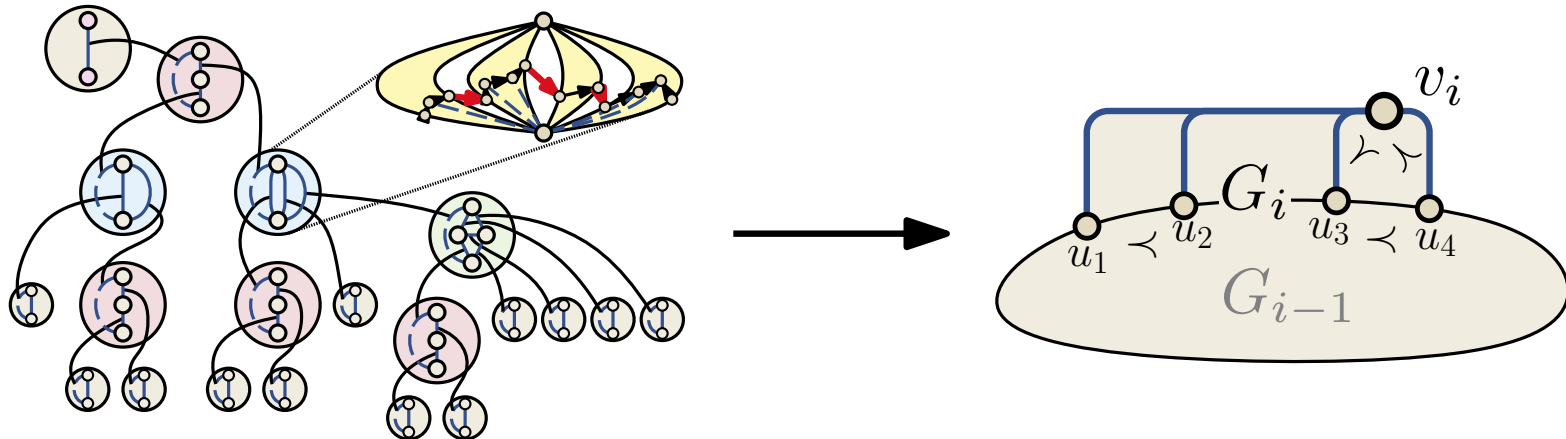


R-node: more complicated, see paper

Theorem: It can be tested in linear time whether a planar st -graph G admits an upward-planar L-drawing.
If it does, it can also be constructed in linear time.

Proof: (sketch)

- process SPQR-tree to find v -bitonic augmentation G^* and embedding \mathcal{E}^* in root node (if any)
- any st -ordering π of G^* yields bitonic pair (\mathcal{E}, π) of G
- G has bitonic pair $\Leftrightarrow G$ admits upward-planar L-drawing
- all steps can be implemented in linear time

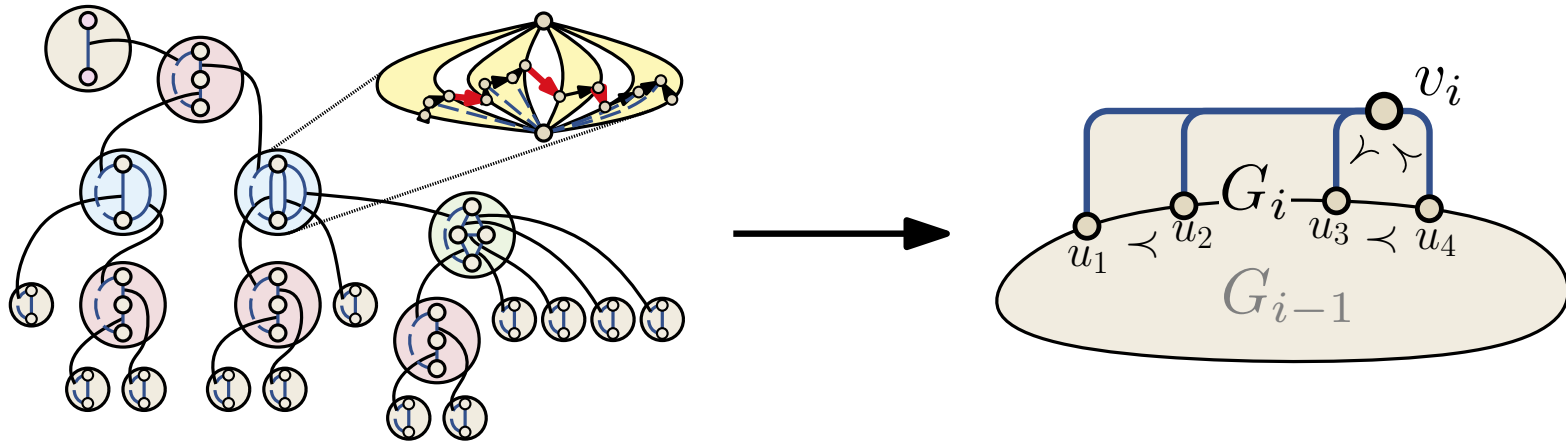


□

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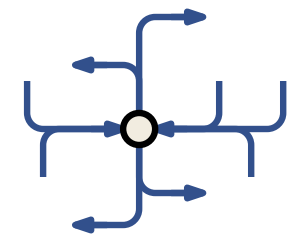


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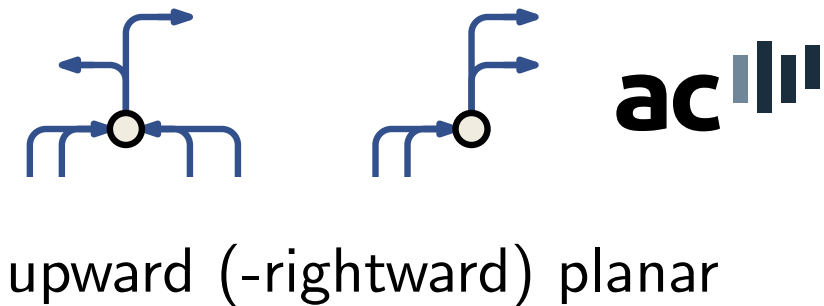
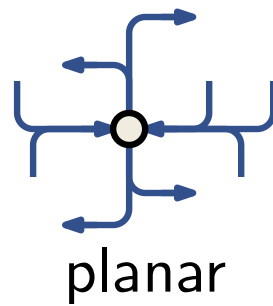


□

Remark: Same approach can be used to decide existence and construct upward-rightward-planar L-drawings.

Summary	 <p>planar</p>	 <p>upward (-rightward) planar</p> 
directed planar graphs	NP-complete	
planar <i>st</i> -graphs		characterization constructive linear time algorithm
directed plane graphs + port assignment	linear time → see paper	
directed plane graphs upward planar graphs bimodal graphs		

Summary



directed planar graphs

NP-complete

planar *st*-graphs

characterization
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directed plane graphs
+ port assignment

linear time
→ see paper

directed plane graphs
upward planar graphs
bimodal graphs

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